

In the following table, the Kernel PLC are divided by memory map :

1) Two PLC belonging to the same column (that's have the same type of memory map) will also have software fully compatible between them.

2) Use a software to a PLC with a different memory map from the PLC for which it was originally created this application program, it requires some adjustments and tune-ups. In this connection, in case you plan to use the application created on another memory map, is strongly recommended to use EDITOR SYMBOLIC that will allow quickly and intuitively telocation of DATA MEMORY.

Division PLC by MEMORY MAP							
STANDARD	D	FUJITSU	36109	ARM Series 100	ARM Series 200	ARM Series 400	ARM Series 700
DMX_16	DMX_16D	GTP_80	KS381C	DR_112R	DP_120	DP_364	KS_574
DMX_19	DMX_19D	KS_392	KS381M	DR_120R	DP_216	KS_521	KS_593
DMX_20	DMX_30DR	KS_392B	KS381N	KS_483	DP_232	KS_544	TP_740
DMX_30R	DMX_30DS	KS_392I	KS381S	KS_497	KS_531	TP_320	
DMX_30S	DMX_32D	KS_392M	KS391	KS_522	KS_550	TP_430	
DMX_32	DMX32D	KS_407	KS391M	KS_546	KS_553		
GTP_64	GTS_128D	KS_408	KS391N	KS_551	KS_556		
GTP_128	GTS_240D	KS_412B	KS391O	T_322	KS_628		
TSP_128	KS_367D	KS_412R	KS_413	VP_116	DMX_16N		
VTP_322	KS_395D	TSP_240	KS_433Q		VTP_323		
VTP_402	KS_405AD	TSP_350	KS_433R				
VTP_403	KS_405D	TSP_570	KS_433S				
	KS_405M		KS_441				
	KS_405P		KS_444				
	KS_414A						
	KS_414P						
	KS_427						
	KS_438						
	KS_443						
	KS_443R						
	KS_456						
	KS_457						
	KS_467						
	PAN_16D						
	TSP_128D						
	VTP_403D						